

PALM INTRANET

Day : Monday  
 Date: 3/5/2007  
 Time: 15:52:33

## Inventor Name Search Result

Your Search was:

Last Name = RIVES

First Name = ERIC

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08871094</a>	<a href="#">5950371</a>	150	06/06/1997	COLUMN MOUNTABLE SHELF FOR FURNITURE SYSTEMS	RIVES, ERIC C.
<a href="#">29070165</a>	<a href="#">D415901</a>	150	05/01/1997	PARTITION	RIVES, ERIC C.
<a href="#">29103160</a>	<a href="#">D431739</a>	150	04/09/1999	FURNITURE LEG	RIVES, ERIC C.
<a href="#">29107710</a>	<a href="#">D428713</a>	150	07/12/1999	SEATING UNIT	RIVES, ERIC C.
<a href="#">60045407</a>	Not Issued	159	05/01/1997	GROUP WORK FURNITURE	RIVES, ERIC C.
<a href="#">09224900</a>	<a href="#">6352323</a>	150	12/31/1998	MEDIA PRESENTATION SYSTEM	RIVES, ERIC CHARLES
<a href="#">09538673</a>	<a href="#">6771715</a>	150	03/30/2000	DEMODULATOR USING CORDIC ROTATOR-BASED DIGITAL PHASE LOCKED LOOP FOR CARRIER FREQUENCY CORRECTION	RIVES, ERIC M
<a href="#">09602850</a>	<a href="#">6801539</a>	150	06/26/2000	SERIALIZED HD SL MULTIPLEXER - DEMULTIPLEXER PROTOCOL	RIVES, ERIC M.
<a href="#">10364144</a>	Not Issued	61	02/11/2003	System and method for recovering a payload data stream from a framing data stream	RIVES, ERIC M.
<a href="#">10620145</a>	Not Issued	93	07/15/2003	DATA BUFFER-CONTROLLED DIGITAL CLOCK REGENERATOR	RIVES, ERIC M.
<a href="#">10620151</a>	Not Issued	71	07/15/2003	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line	RIVES, ERIC M.
<a href="#">10839533</a>	Not Issued	161	05/05/2004	Demodulator using cordic rotator-based digital phase locked	RIVES, ERIC M.

				loop for carrier frequency correction	
<u>09771370</u>	<u>6907267</u>	150	01/26/2001	LOOP-POWERED T1 RADIO	RIVES, ERIC MALCOLM
<u>10462921</u>	Not Issued	30	06/17/2003	Use of T1 facility data link to transport status and configuration data packets for loop-powered T1 radio	RIVES, ERIC MALCOLM

**Inventor Search Completed: No Records to Display.**

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## Inventor Name Search Result

Your Search was:

Last Name = KLIESNER

First Name = MATTHEW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10364144</a>	Not Issued	61	02/11/2003	System and method for recovering a payload data stream from a framing data stream	KLIESNER, MATTHEW A.
<a href="#">10613384</a>	<a href="#">6819133</a>	150	07/03/2003	SYSTEM AND METHOD FOR PROTECTING CONFIGURATION DATA FOR A PROGRAMMABLE EXECUTION UNIT	KLIESNER, MATTHEW A.
<a href="#">10620145</a>	Not Issued	93	07/15/2003	DATA BUFFER-CONTROLLED DIGITAL CLOCK REGENERATOR	KLIESNER, MATTHEW A.
<a href="#">10620151</a>	Not Issued	71	07/15/2003	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line	KLIESNER, MATTHEW A.
<a href="#">10839491</a>	Not Issued	30	05/05/2004	T1 radio having coding gain adjustable for number of channels	KLIESNER, MATTHEW A.
<a href="#">10839496</a>	Not Issued	161	05/05/2004	Radio diagnostic tool combining signal quality and signal strength measurement	KLIESNER, MATTHEW A.

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**PALM INTRANET**Day : Monday  
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Your Search was:

Last Name = MESTER

First Name = TIMOTHY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10613384</a>	<a href="#">6819133</a>	150	07/03/2003	SYSTEM AND METHOD FOR PROTECTING CONFIGURATION DATA FOR A PROGRAMMABLE EXECUTION UNIT	MESTER, TIMOTHY G.
<a href="#">10620145</a>	Not Issued	93	07/15/2003	DATA BUFFER-CONTROLLED DIGITAL CLOCK REGENERATOR	MESTER, TIMOTHY G.
<a href="#">10620151</a>	Not Issued	71	07/15/2003	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line	MESTER, TIMOTHY G.
<a href="#">10839491</a>	Not Issued	30	05/05/2004	T1 radio having coding gain adjustable for number of channels	MESTER, TIMOTHY G.
<a href="#">10839496</a>	Not Issued	161	05/05/2004	Radio diagnostic tool combining signal quality and signal strength measurement	MESTER, TIMOTHY G.

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